

Patent Abstracts of Japan

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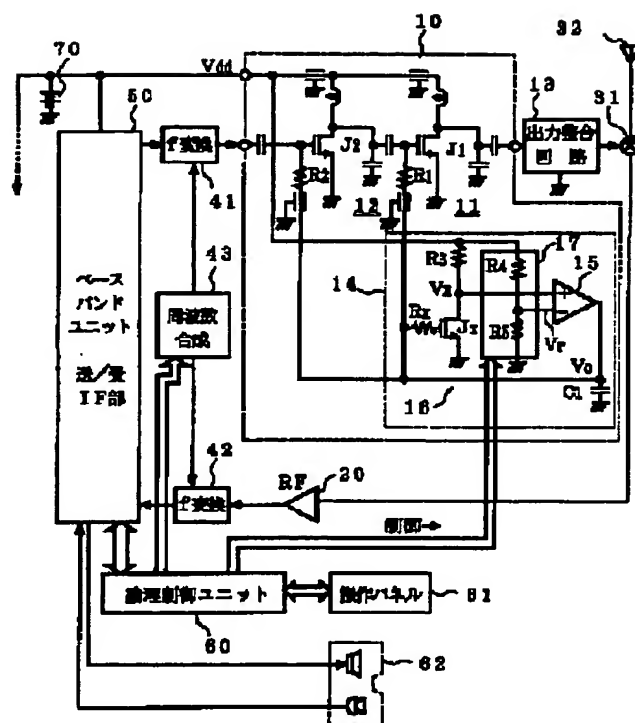
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TITLE : RF POWER AMPLIFIER CIRCUIT AND
MOBILE COMMUNICATION TERMINAL
EQUIPMENT



ABSTRACT : PROBLEM TO BE SOLVED: To attain RF power amplification in a stable and efficient state under a single power supply without troublesome and hairbreadth adjustment such as trimming by providing an output voltage of a comparison amplifier circuit forming a DC feedback circuit to a gate of a power amplifier FET as a bias voltage.

SOLUTION: An output voltage V_o of a comparison amplifier circuit 15 from a bias generating circuit 14 is given to enhancement GaAs FETs J1, J2 forming respectively a pre-stage 12 and a final stage 11 of an RF power amplifier circuit 10 as a gate bias voltage and the FETs J1, J2 amplify power of a radio signal converted into a prescribed transmission frequency at a frequency conversion circuit 41 into a prescribed level. The output voltage V_o of the comparison amplifier circuit 15 of the bias generating circuit 14 is given to a gate of a dummy FET Jx as a bias voltage to form a DC negative feedback circuit 16.

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